L Number	Hits	Search Text	DB	Time stamp
1	1971		USPAT:	2001/11/21 11:12
		1	US-PGPUB;	2001/11/21 11:12
			EPO; JPO;	
			DERWENT;	1
ļ			IBM TDB	ļ
2	28	(analog adj communicat\$4) and ((isolation	USPAT;	2001/11/21 11:28
		adj barrier) or (isolation adj system))	US-PGPUB;	2001/11/21 11:28
		· · · · · · · · · · · · · · · · · · ·	EPO; JPO;	
			DERWENT;	
			IBM TDB	
3	24	((analog adj communicat\$4) and ((isolation	USPAT;	2001/11/21 11:29
		adj barrier) or (isolation adj system)))	US-PGPUB;	2001/11/21 11:29
,		and analog adj output	EPO; JPO;	ļ
		and cacpac	DERWENT;	[
			IBM TDB	
4	7	(((analog adj communicat\$4) and	USPAT;	2001/11/21 11:30
		((isolation adj barrier) or (isolation adj	US-PGPUB;	2001/11/21 11.30
1		system))) and analog adj output) and input	EPO; JPO;	
I		adj digital	DERWENT:	1 1
			IBM TDB	i l
5	7	((((analog adj communicat\$4) and	USPAT:	2001/11/21 11:31
		((isolation adj barrier) or (isolation adj	US-PGPUB;	=====================================
1	ļ	system))) and analog adj output) and input	EPO; JPO;	
ļ	Ī	adj digital) and digital adj output	DERWENT:	
		· · · · · · · · · · · · · · · · · ·	IBM TDB	
6	6	(((((analog adj communicat\$4) and	USPAT:	2001/11/21 11:32
ļ		((isolation adj barrier) or (isolation adj	US-PGPUB:	=====================================
1	•	system))) and analog adj output) and input	EPO; JPO;	1
!		adj digital) and digital adj output) and	DERWENT;	
ļ		((digital adj to adj analog) or (DAC))	IBM TDB	
7	6	((((((analog adj communicat\$4) and	USPAT;	2001/11/21 11:33
1		((isolation adj barrier) or (isolation adj	US-PGPUB;	
		system))) and analog adj output) and input	EPO; JPO;	
		adj digital) and digital adj output) and	DERWENT;	1
]	ļ	((digital adj to adj analog) or (DAC)))	IBM TDB	[
		and ((analog adj to adj digital) or (ADC))		

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Today's Date: 11/21/2001

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USPT	L16 and transformer	5	<u>L32</u>
USPT	L16 and capacitive isolation	1	<u>L31</u>
USPT	L16 and analog communication	0	<u>L30</u>
USPT	L27 and digital output	1	<u>L29</u>
USPT	L27 and output digital	0	<u>L28</u>
USPT	L22 and analog output	2	<u>L27</u>
USPT	L22 and anaog output	0	<u>L26</u>
USPT	L22 and analog to digital	0	· <u>L25</u>
USPT	L22 and ADC	0	<u>L24</u>
USPT	L22 and DAC	0	<u>L23</u>
USPT	L16 and capacitor	5	<u>L22</u>
USPT	L17 and input digital	0	<u>L21</u>
USPT	L17 and digital to analog	0	<u>L20</u>
USPT	L17 and DAC	0	<u>L19</u>
USPT	L17 and analog output	0	<u>L18</u>
USPT	L2 and capacitor	2	<u>L17</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	analog isolation circuit	7	<u>L16</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L14 and analog isolation circuit	0	<u>L15</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	analog isolation barrier	2	<u>L14</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L12 and ADC	6	<u>L13</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L11 and DAC	6	<u>L12</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L9 and digital output	6	<u>L11</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L9 and input digital	0	<u>L10</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L7 and analog output	6	<u>L9</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L1 and (analog isolation circuit)	0	<u>L8</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L1 and (analog isolation system)	6	<u>L7</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L1 and (analog isolation barrier)	0	<u>L6</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L and (analog isolation barrier)	0	<u>L5</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L and (analog isolation barrier)	0	<u>L4</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	L and (analog isolation interface)	0	<u>L3</u>
UȘPT,PGPB,JPAB,EPAB,DWPI,TDBD	L1 and (analog communication interface)	12	<u>L2</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	analog communicat\$4	2139	<u>L1</u>

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